

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	4	huang-cheng-yen.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 13:13
S2	2156	365/154.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 13:14
S3	7601	access ADJ transistor\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 13:15
S4	7448	("bit line" bit-line) ADJ pair	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 13:15
S5	1122	latch\$3 ADJ node	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 13:15
S6	1669	flush\$3 ADJ line	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 13:16
S7	72816	select\$3 ADJ (unit device)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 13:31
S8	42262	cach\$3 ADJ memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 13:17

S9	1	S3 S5 S7 S6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/10/05 13:17
S10	121	S3 S5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/10/05 13:18
S11	72	S8 S6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/10/05 13:43
S12	147109	select\$3 ADJ (unit device circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 13:31
S13	41	S3 S5 S12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/10/05 13:32
S14	28	S12 S6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/10/05 13:53
S15	1911	(SRAM "static random access memory") AND S7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 13:46
S16	23	S15 S5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/10/05 13:45

S17	8	S15 S5 S3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2005/10/05 13:45
S18	23	(SRAM "static random access memory") AND S6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 15:00
S21	35	(flush\$3 ADJ line) AND ("OR" ADJ gate)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 14:43
S22	221	("parity bit" "control field") AND ("tag memory")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 14:04
S23	332	711/135.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 14:08
S25	51	("OR" ADJ gate) WITH flush	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 15:01
S26	168	flip-flop SAME driv\$3 ADJ buffer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 15:36
S27	68	flip-flop WITH driv\$3 ADJ buffer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 15:22

S28	3	"D flip-flop" SAME driv\$3 ADJ buffer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 15:38
S29	2	"DFF" SAME driv\$3 ADJ buffer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 15:37
S30	8	"D-type flip-flop" SAME driv\$3 ADJ buffer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/05 15:38
S31	1089	365/218.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/06 14:14
S32	7611	access ADJ transistor\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/06 14:15
S33	17	S31 AND S32	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/06 14:15